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SPECIFICATION

VXT700TOSA-01

☐ Preliminary Specification

☐ Final Specification

Approved By: **Date:**

RECORD OF REVISION

Rev No.	Rev Date	Page	Contents	Editor
V00	2025/4/21		New issue.	Solon

Victronix Display Co., Limited Tel:86-755-33265935 Email:sales@victronix-tech.com

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1. Scope

This specification defines general provisions as well as inspection standards for TFT module supplied by Victronix.
If the event of unforeseen problem or unspecified items may occur, naturally shall negotiate and agree to solution.

2. General Specifications

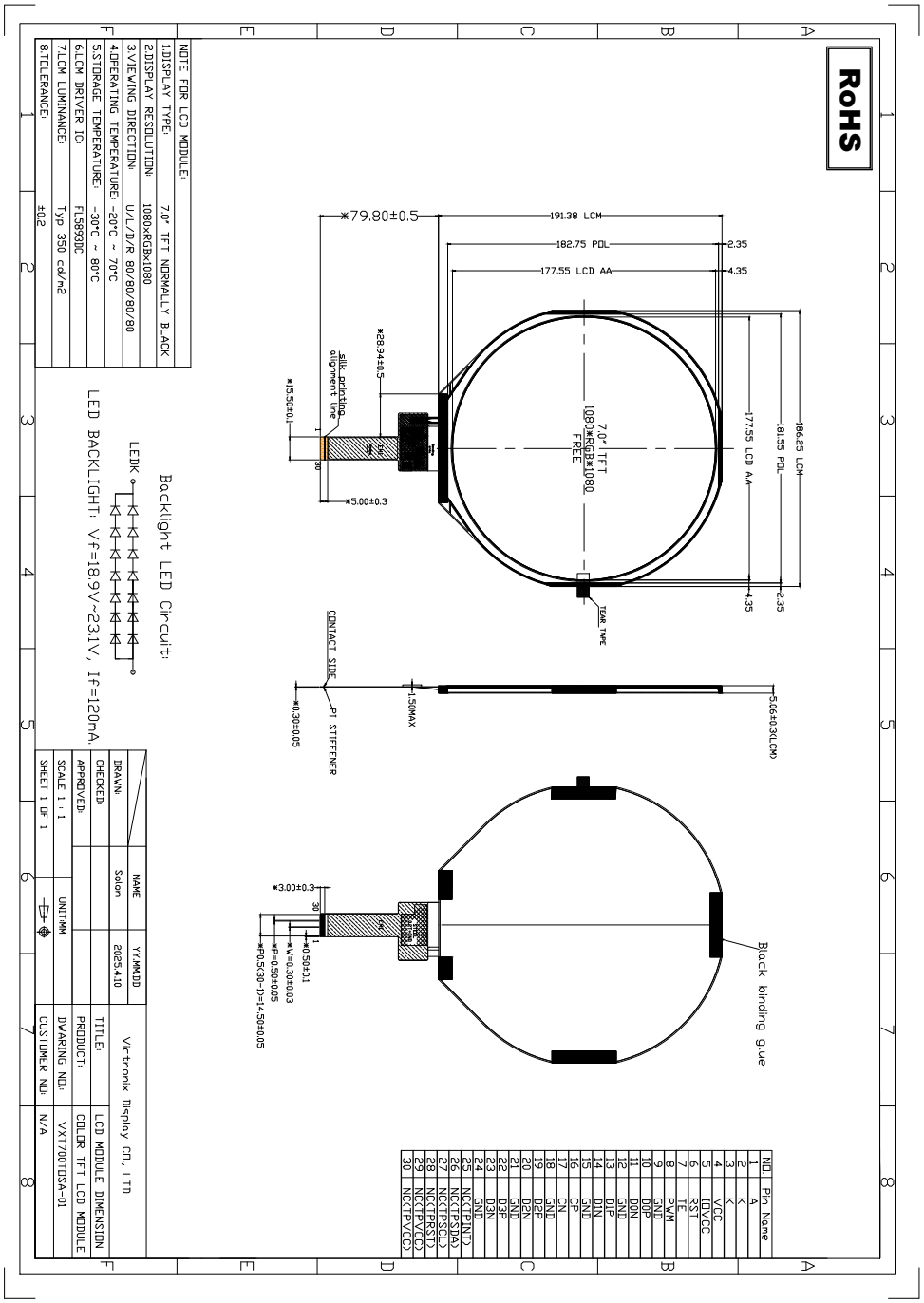
2.1 LCD Parameter

Item	Contents	Unit	Note
LCD Type	TFT	-	
Display color	16.7M		
Viewing Direction	ALL	O'Clock	
Operating temperature	-20~+70	℃	
Storage temperature	-30~+80	℃	
Module size	7.0	inch	
Active Area(W×H)	177.55x177.55	mm	
Number of Dots	1080x1080	dots	
Power Supply Voltage	3.3	V	
Outline Dimensions	186.25x191.38x5.06	mm	
TFT Controller	FL5893DC	-	
Backlight	8X4-LEDs (white)	pcs	
Interface	4 LANE MIPI	-	

Note 1: Color tune is slightly changed by temperature and driving voltage.

Note 2: Without FPC and Solder.

3. Outline Drawing



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4.Interface Description

4.1 LCD interface

Pin No.	Symbol	I/O	Function	Note
1	A	P	LED anode.	
2-3	K	P	LED cathode.	
4	VCC	P	Power supply.	
5	IOVDD	P	Digital I/O power supply.	
6	RST	I	Reset pin. Setting either pin low initializes the LSI. Must be reset after power is supplied	
6	NC	-	No connection.	
7	TE	P	Tearing effect output.	
8	PWM	O	PWM control signal for LED driver (CABC) .	
9	GND	P	Ground.	
10	D0P	I	MIPI Data differential signal input pins. (Data lane 0).	
11	D0N	I	MIPI Data differential signal input pins. (Data lane 0).	
12	GND	P	Ground.	
13	D1P	I	MIPI Data differential signal input pins. (Data lane 1).	
14	D1N	I	MIPI Data differential signal input pins. (Data lane 1).	
15	GND	P	Ground.	
16	CP	I	MIPI CLOCK differential signal input pins.	
17	CN	I	MIPI CLOCK differential signal input pins.	
18	GND	P	Ground.	
19	D2P	I	MIPI Data differential signal input pins. (Data lane 2).	
20	D2N	I	MIPI Data differential signal input pins. (Data lane 2).	
21	GND	P	Ground.	
22	D3P	I	MIPI Data differential signal input pins. (Data lane 3).	
23	D3N	I	MIPI Data differential signal input pins. (Data lane 3).	
24	GND	P	Ground.	
25	TP_INT	O	External Interrupt pin.	
26	TP_SDA	I/O	Serial input/output data bus.	
27	TP_SCL	I	Serial interface clock.	
28	TP_RST	I	Reset signal.	
29-30	TP_VDD	I	Power supply for digital circuits .	

4.2 CTP PIN

Pin No.	Symbol	I/O	Function
1	GND	P	Ground.
2	TP_INT	O	External Interrupt pin.
3	TP_SDA	I/O	Serial input/output data bus.
4	TP_SCL	I	Serial interface clock.
5	TP_VCC	I	Power supply for digital circuits .
6	TP_RESET	I	Reset signal.
7	TP_VCC	I	Power supply for digital circuits .

5. Absolute Maximum Ratings(Ta=25℃)

5.1 Electrical Absolute Maximum Ratings.(Vss=0V ,Ta=25℃)

Item	Symbol	Min.	Max.	Unit	Note

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Power Supply Voltage	VDDI	-0.3	4.0	V	1，2
	VDDRX	-0.3	4.0		
	VGH-VGL	-0.5	+35.2		

Notes:

1. If the module is above these absolute maximum ratings. It may become permanently damaged. Using the module within the following electrical characteristic conditions are also exceeded, the module will malfunction and cause poor reliability.
2. V_{DD} >V_{SS} must be maintained.

5.2 Environmental Absolute Maximum Ratings.

Item	Storage		Operating		Note
	MIN.	MAX.	MIN.	MAX.	
Ambient Temperature	-30℃	80℃	-20℃	70℃	1,2
Humidity	-	-	-	-	3

1. The response time will become lower when operated at low temperature.
2. Background color changes slightly depending on ambient temperature.
   The phenomenon is reversible.
3. Ta<=40℃:85%RH MAX.
   Ta>=40℃:Absolute humidity must be lower than the humidity of 85%RH at 40℃.

6. Electrical Specifications and Instruction Code

6.1 Electrical characteristics(Vss=0V ,Ta=25℃)

In accordance with the Absolute Maximum Rating System; please refer to notes 1 and 2.

Parameter	Symbol	Conditions	Unit
I/O Power Supply Voltage	VDDI	-0.3 ~ 4.0	V
Interface Power supply voltage	VDDRX	-0.3 ~ 4.0	V
Analog Power supply voltage	VDDP, VDD_PFM	-0.3 ~ 4.0	V
	VSP	-0.3 ~ 7.0	V
	VSN	-7.0 ~ -0.3	V
	VGH – VGL	-0.5 ~ 35.2	V
SPI and I2C Interface Input Voltage	V_{IN}	-0.3 ~ VDDI +0.3	V
OTP Power supply voltage	VDD_OTP	-0.3 ~ 8.5	V
Storage temperature	T_{STR}	-55 ~ 125	°C
Junction temperature	T_{JC}	-40~125	°C

Notes:

1. Stresses exceed the absolute maximum ratings listed above may cause permanent damage to IC. The IC should be operated under the condition of DC/AC characteristics for normal operation. If this condition is not met, the IC may be malfunctioned, or the reliability may drop.
2. Parameters are valid in the operating temperature range unless otherwise specified. All voltages are with respect to VSS unless otherwise noted.

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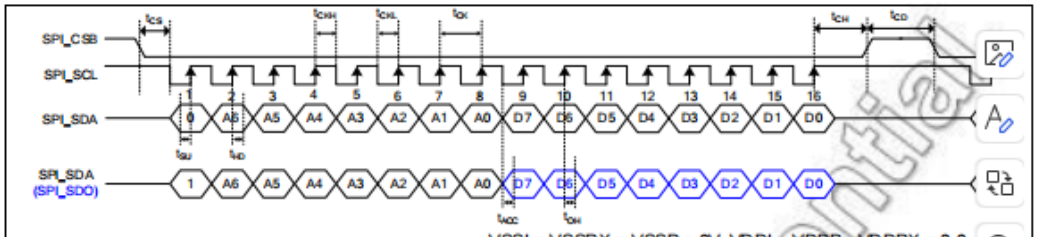
7. Timing Characteristics

7.1 AC Characteristics

7.1.1 SPI AC Timing

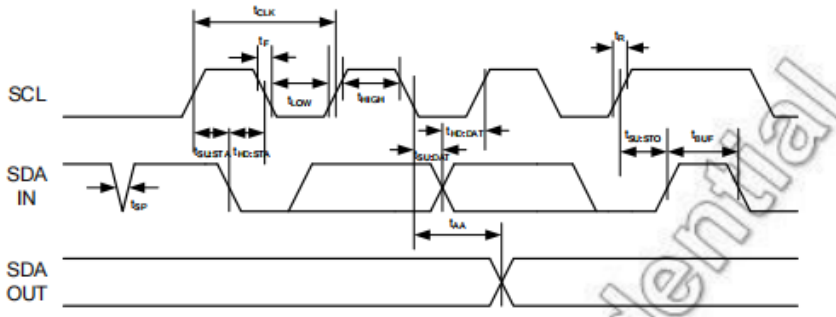


VSSI = VSSRX = VSSP = 0V, VDDI = VDDP= VDDRX = 3.0 ~

Parameter	Signal	Symbol	Min	Typ.	Max.	Unit	Condition
Serial clock period (Write)	SPI_SCL	t_{CK}	250	-	-	ns	
Serial clock period (Read)			250	-	-		
SCL "H" pulse width (Write)		t_{CKH}	125	-	-		
SCL "H" pulse width (Read)			125	-	-		
SCL "L" pulse width (Write)		t_{CKL}	125	-	-		
SCL "L" pulse width (Read)			125	-	-		
Write data input setup time	SPI_SDA	t_{SU}	100	-	-		
Write data input hold time		t_{HD}	100	-	-		
Read Data access time		t_{ACC}	-	-	100		
Read Data output disable time		t_{OH}	20	-	100		
SCL-SDO output delay time		t_{DLY}	-	-	50		
CSB-SCL time	SPI_CSB	t_{CS}	330	-	-		
CSB-SCL time		t_{CH}	250	-	-		
CSB "H" pulse width		t_{CD}	1000	-	-		

Note:
1. The input signal rise and fall time (tr, tf) are specified at 15ns or less.
2. All timing is specified using 20% and 80% of VDDI as the standard.

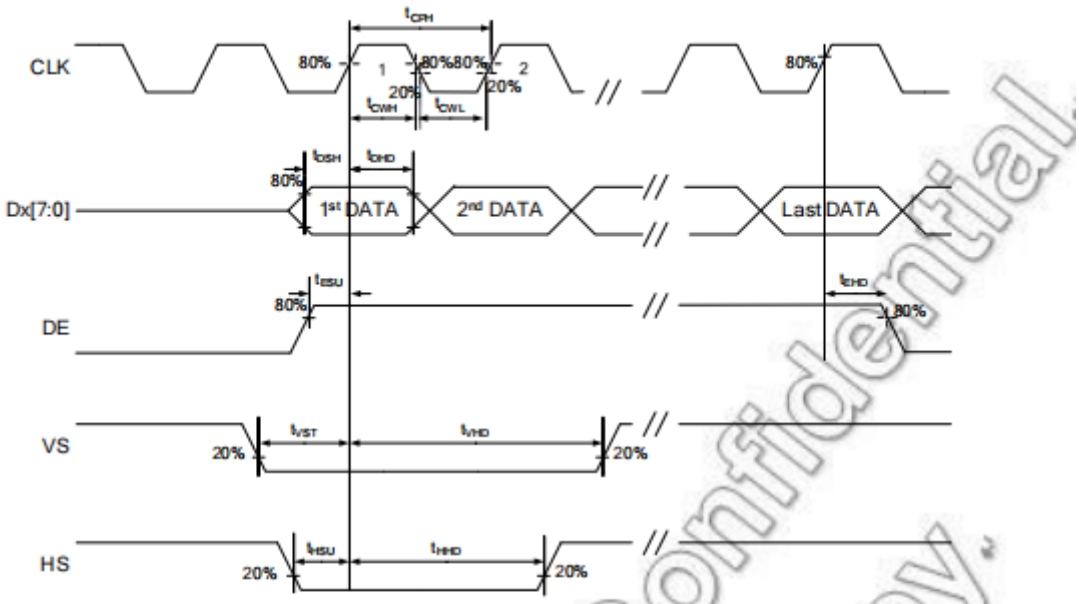
7.1.2 I2C AC Timing



VSSI = VSSRX = VSSP = 0V, VDDI = VDDP= VDDRX = 3.0 ~ 3.6V

Item	Signal	Symbol	Condition	Rating Min.	Rating Max.	Unit
Clock frequency	SCL	f_{CLK}		2500		ns
Clock high time		t_{HIGH}		1250		
Clock low time		t_{LOW}		1250		
SDA and SCL rise time		t_R			300	
SDA and SCL fall time		t_F			300	
Start condition hold time	SDA IN	$t_{HD:STA}$		600		
Start condition setup time		$t_{SU:STA}$		600		
Data input hold time		$t_{HD:DAT}$		0		
Data input setup time		$t_{SU:DAT}$		100		
Stop condition setup time		$t_{SU:STO}$		600		
Output valid from clock		t_{AA}			900	
Input filter spike suppression (SDA and SCL pins)		t_{SP}			50	
Bus free-time: Time the bus must be free before a new transmission can start	SDA OUT	t_{BUF}		1300		

7.1.3 CMOS AC Timing



VSSI = VSSRX = VSSP = 0V, VDDI = VDDP= VDDRX = 3.0 ~ 3.6V

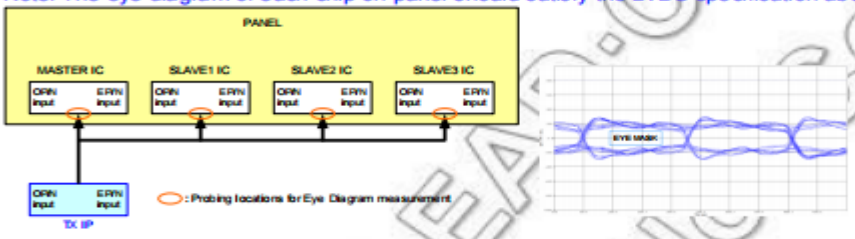
Item	Signal	Symbol	Condition	Rating		Unit
				Min.	Max.	
CLK cycle time	CLK	t_{CPH}		20	200	ns
CLK pulse high duty		t_{CWH}		40	60	%
CLK pulse low duty		t_{CWL}		40	60	
VS setup time	VS	t_{VST}		4	-	ns
VS hold time		t_{VHD}		2	-	
HS setup time	HS	t_{HST}		4	-	
HS hold time		t_{HHD}		2	-	
Data setup time	Dx[7:0]	t_{DSH}		4	-	
Date hold time		t_{DHD}		2	-	
DE setup time	DE	t_{ESU}		4	-	
DE hold time		t_{EHD}		2	-	

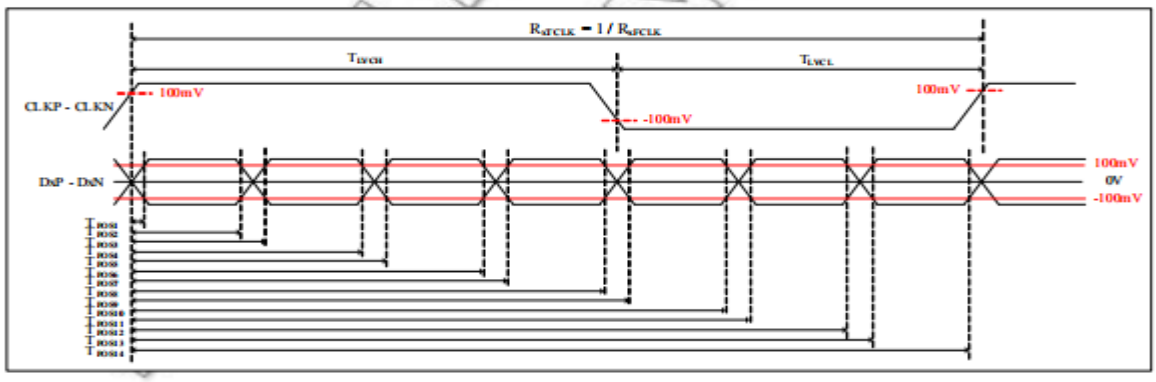
7.1.4 LVDS AC Timing

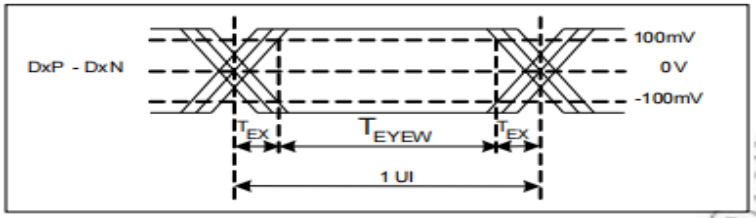
VSSI = VSSRX = VSSP = 0V, VDDI = VDDP= VDDRX = 3.0 ~ 3.6V

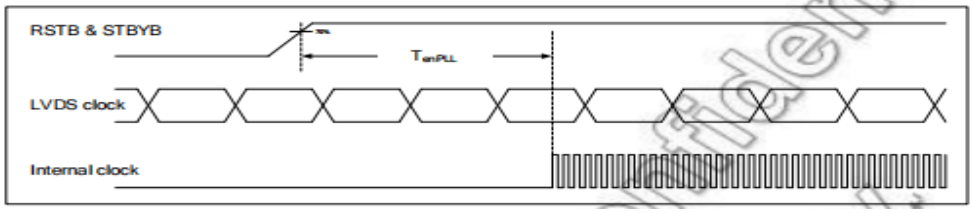
Item	Signal	Symbol	Rating			Unit
			Min.	Typ.	Max.	
Clock Frequency	CLK	R_{xFCLK}	20	-	100	MHz
Clock Period		R_{xTCLK}	10	-	50	ns
1 data bit time		UI	-	1/7	-	R_{xTCLK}
Clock high time	CLK	T_{LVCH}		4		UI
Clock low time		T_{LVCL}		3		UI
Position 1	DATA	T_{POS1}	-0.25	0	0.25	UI
Position 2		T_{POS2}	0.75	-	1.25	
Position 3		T_{POS3}	0.75	1	1.25	
Position 4		T_{POS4}	1.75	-	2.25	
Position 5		T_{POS5}	1.75	2	2.25	
Position 6		T_{POS6}	2.75	-	3.25	
Position 7		T_{POS7}	2.75	3	3.25	
Position 8		T_{POS8}	3.75	-	4.25	
Position 9		T_{POS9}	3.75	4	4.25	
Position 10		T_{POS10}	4.75	-	5.25	
Position 11		T_{POS11}	4.75	5	5.25	
Position 12		T_{POS12}	5.75	-	6.25	
Position 13		T_{POS13}	5.75	6	6.25	
Position 14		T_{POS14}	6.75	-	7.25	
Input eye width		T_{EYEW}	0.5	-	-	
Input eye border		T_{EX}	-	-	0.25	
PLL wake-up time		TenPLL	-	-	150	us

Note: The eye diagram of each chip on panel should satisfy the LVDS specification above.

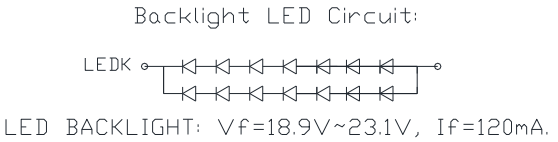








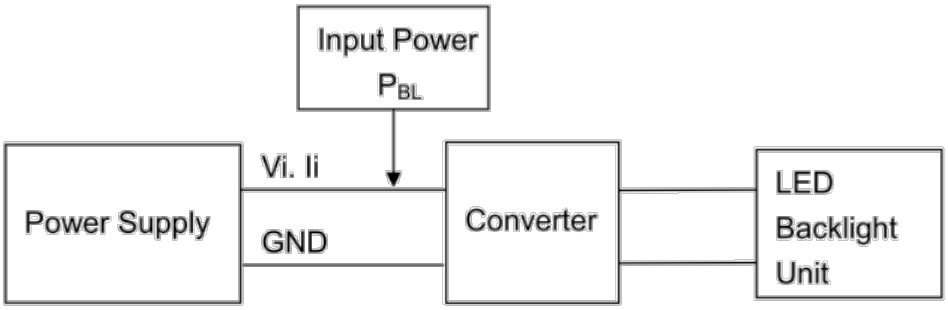
8.0 Backlight Characteristic



Item	Symbol	Min	Typ	Max	Unit	Test Condition
Supply Voltage	Vf	18.9	21	23.1	V	Note 1
Supply Current	If	-	120	-	mA	Note 2
Power dissipation	P_{BL}	-	2.52	-	W	
Life Time	-	30000	-	-	Hr	Note 3,4
Backlight Color	White					

Note 1: The LED Supply Voltage is defined by the number of LED at Ta=25℃ and If =80mA.

Note 2: LED current is measured by utilizing a high frequency current meter as shown below:



Note 3: The “LED life time” is defined as the module brightness decrease to 50% original brightness at Ta=25℃ and If =80mA. The LED lifetime could be decreased if operating If is larger than 80mA.
Note 4: LED light bar circuit:

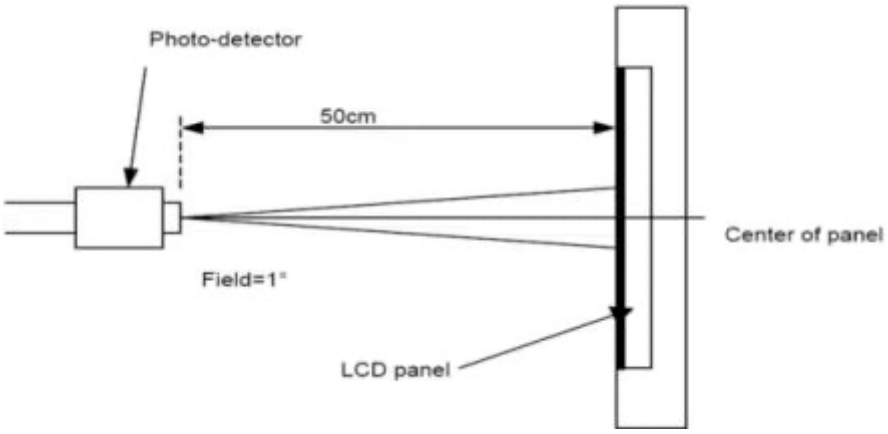
9. Optical Characteristics

Item	Symbol		Condition	Min.	Typ.	Max.	Unit	Note
Brightness	Bp		If=80mA	400	500	-	Cd/m^2	1
Uniformity	⊿Bp			70	-	-	%	1,2
Viewing Angle	3:00		Cr≥10	-	80	-	Deg	1,2
	6:00			-	80	-		
	9:00			-	80	-		
	12:00			-	80	-		
Contrast Ratio	Cr		θ=0° Φ=0°	600	800	-	-	3,4
Response Time	T_r+T_f			-	20	25	ms	4,5
Color of CIE Coordinate	W	x	θ=0° Φ=0°	-	TBD	-	-	1,6
		y		-	TBD	-	-	
	R	x		-	TBD	-	-	
		y		-	TBD	-	-	
	G	x		-	TBD	-	-	
		y		-	TBD	-	-	
	B	x		-	TBD	-	-	
		y		-	TBD	-	-	
NTSC Ratio	S			-	55	-	%	

*The parameter is slightly changed by temperature, driving voltage and materiel

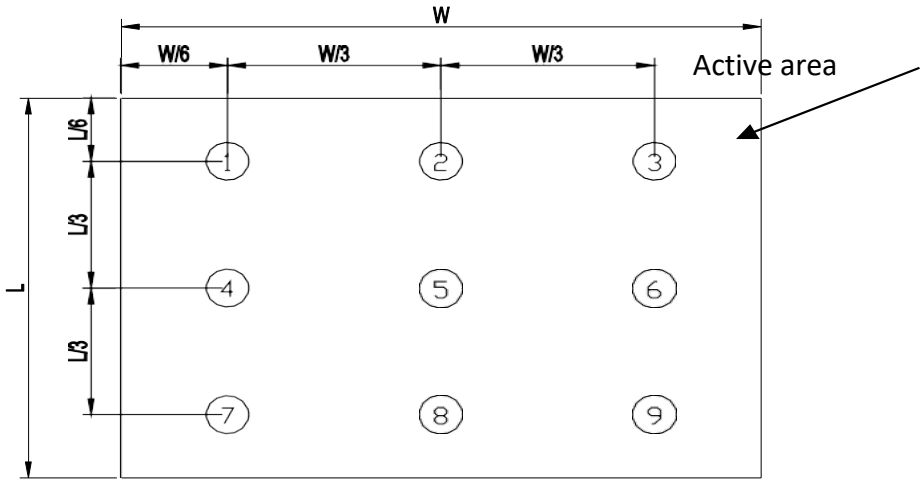
Note 1: The data are measured after LEDs are turned on for 5 minutes. LCM displays full white. The brightness is the average value of 9 measured spots. Measurement equipment CA310 Measuring condition:-Measuring surroundings: Dark room.-Measuring temperature: Ta=25℃.-Adjust operating voltage to get optimum contrast at the center of the display.

The measured value is more than 5 minutes at the center point of the LCD panel, and the backlight is turned on at the same time.

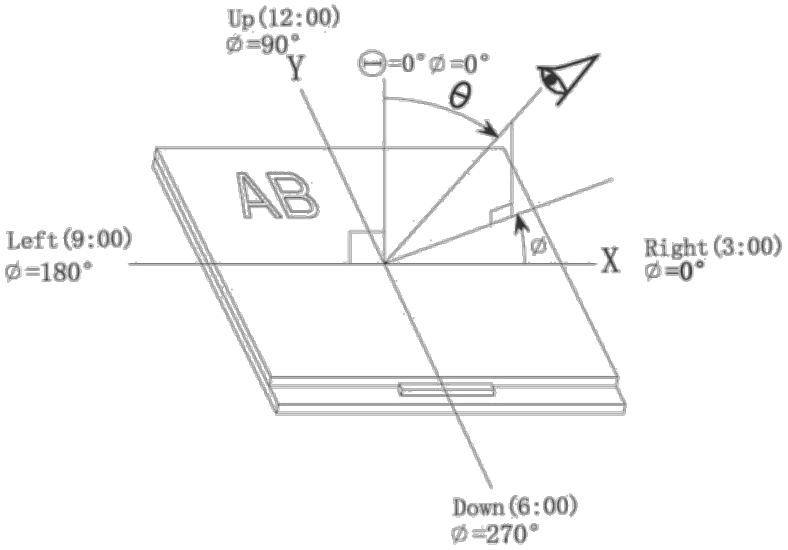


Note 2: The luminance uniformity is calculated by using following formula.

⊿Bp = Bp (Min.) / Bp (Max.)×100 (%);Bp (Max.) = Maximum brightness in 9 measured spots Bp (Min.) = Minimum brightness in 9 measured spots.



Note 3: The definition of viewing angle:Refer to the graph below marked by θ and Φ

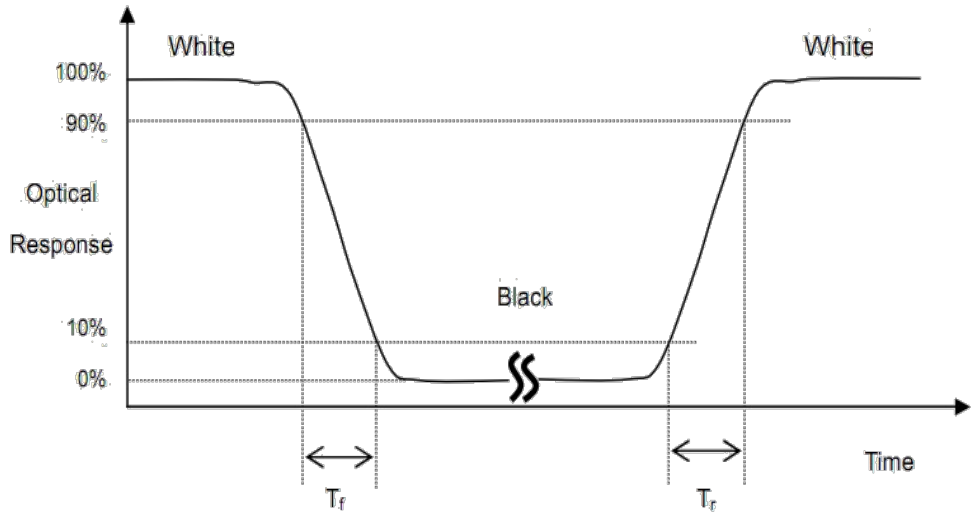


Note 4: Definition of contrast ratio Contrast measurements shall be made at viewing angle of Θ= 0 and at the center of the LCD surface.Luminance shall be measured with all pixels in the view field

set first to white, then to the dark (black) state.

$$\mathbf{CR} = \frac{\text{Luminance when displaying a white raster}}{\text{Luminance when displaying a black raster}}$$

Note 5: Definition of Response time The output signals of photo detector are measured when the input signals are changed from “white” to “black”(Tf) and from “black” to “white”(Tr), respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes. Refer to figure as below.



10. Reliability Test Conditions and Methods

No.	Test Items	Test Condition	Inspection After Test
①	High Temperature Storage	60°C±2°C×240Hours	Inspection after 2~4hours storage at room temperature, the samples should be free from defects: 1, Air bubble in the LCD. 2, Seal leak. 3, Non-display. 4, Missing segments. 5, Glass crack. 6, Current IDD is twice higher than initial value. 7, The surface shall be free from damage. 8, The electric characteristic requirements shall be satisfied. 9.Brightness reduction more than 50%.
②	Low Temperature Storage	-10°C±2°C×240Hours	
③	High Temperature Operating	50°C±2°C×240Hours	
④	Low Temperature Operating	0°C±2°C×240Hours	
⑤	Temperature Cycle(Storage)	-10°C ⟺ 60°C (30min) (5min) (30min) 1cycle Total 10cycle.	
⑥	Damp Proof Test (Storage)	40°C±5°C×90%RH×240Hours	
⑦	ESD Test	Contact Discharge:±4KV,R:330Ω,C:150PF, Air Discharge:±8KV,R:330Ω,C:150PF, 5 points, 5 times at each position, 1S apart	

REMARK:

1, The Test samples should be applied to only one test item.

2, Sample side for each test item is 5~10pcs.

3,For Damp Proof Test, Pure water(Resistance＞10MΩ)should be used.

4,In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judge as a good part.

5, EL evaluation should be accepted from reliability test with humidity and temperature: Some defects such as black spot/blemish can happen by natural chemical reaction with humidity and Fluorescence EL has.

6, Failure Judgment Criterion: Basic Specification Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.

11. Inspection Standard

11.1 Scope

Specifications contain
11.1.1 Display Quality Evaluation
11.1.2 Mechanics Specification

11.2 Sampling Plan

Unless there is other agreement, the sampling plan for incoming inspection shall follow MIL-STD-105E.
11.2.1 Lot size: Quantity per shipment as one lot (different model as different lot).
11.2.2 Sampling type: Normal inspection, single sampling.
11.2.3 Sampling level: Level II.
11.2.4 AQL: Acceptable Quality Level
Major defect: AQL=0.65
Minor defect: AQL=1.5

11.3 Panel Inspection Condition

11.3.1 Environment:
Room Temperature: 25±5°C.
Humidity: 65±5% RH.
Illumination: 300 ~ 700 Lux.

11.3.2 Inspection Distance:
35±5 cm

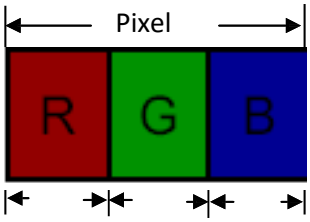
11.3.3 Inspection Angle:
The vision of inspector should be perpendicular to the surface of the Module.
11.3.4 Inspection time：
Perceptibility Test Time: 20 seconds max.

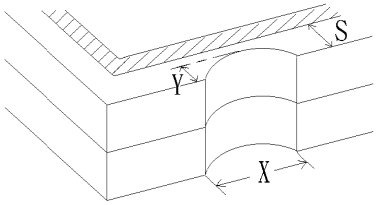
11.4 Inspection Plan

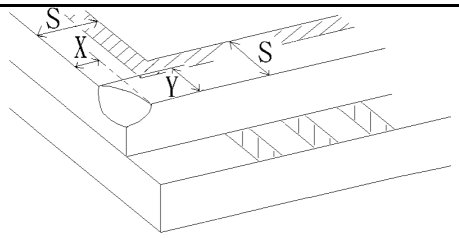
Class	Item	Judgment	Class
Packing & Indicate	1. Outside and inside package.	"MODEL NO.", "LOT NO." and "QUANTITY" should indicate on the package.	Minor
	2. Model mixed and quantity.	Other model mixed Quantity short or over	Major
	3. Product indication.	"MODEL NO." should indicate on the product.	Major
Assembly	4. Dimension, LCD glass scratch and scribe defect.	According to specification or drawing.	Major
Appearance	5. Viewing area.	Polarizer edge or LCD's sealing line is visible in the viewing area…….Rejected.	Minor
	6. Blemish, black spot, white spot in the LCD and LCD glass cracks.	According to standard of visual inspection.(inside viewing area)	Minor
	7. Blemish, black spot, white spot and scratch on the polarizer.	According to standard of visual inspection.(inside viewing area)	Minor
	8. Bubble in polarizer.	According to standard of visual inspection.(inside viewing area)	Minor
	9. LCD's rainbow color.	Strong deviation color (or newton ring) of LCD……..Rejected. Or according to limited sample.(if needed, and inside viewing area)	Minor
Electrical	10. Electrical and optical characteristics.(contrast Vop chromaticity….etc)	According to specification or drawing.(inside viewing area)	Major
	11. Missing line.	Missing dot line character	Major
	12.Short circuit. Wrong pattern display.	No display, wrong pattern display, current consumption. Out of specification	Major
	13. Dot defect.(for color and TFT)	According to standard of visual Inspection.	Minor

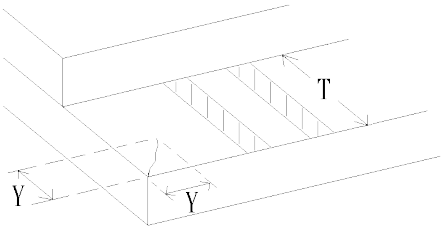
11.5 Standard Of Visual Inspection

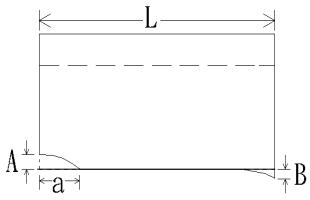
<table>
<tr><th>NO.</th><th>CLASS</th><th>ITEM</th><th>JUDGMENT</th></tr>
<tr><td>11.5.1</td><td>Minor</td><td>Black and white spot.
Foreign materiel.
Dust.
Blemish.
Scratch.</td><td>(A) Round type: Unit: mm
<table>
<tr><th>Diameter (mm.)</th><th>Acceptable Q'ty</th></tr>
<tr><td>Φ≦0.2</td><td>Disregard</td></tr>
<tr><td>0.2 < Φ≦0.5</td><td>2(Distance>10mm)</td></tr>
<tr><td>0.50 < Φ</td><td>0</td></tr>
</table>
Note: Φ = (length+width)/2
(B) Linear type: Unit: mm
<table>
<tr><th>Length</th><th>Width (mm.)</th><th>Acceptable Q'ty</th></tr>
<tr><td>--</td><td>W≦0.05</td><td>Disregard</td></tr>
<tr><td>L≦3.0</td><td>0.05< W≦0.1</td><td>2(Distance>10mm)</td></tr>
<tr><td>--</td><td>0.1 < W</td><td>Not allow</td></tr>
</table></td></tr>
<tr><td>11.5.2</td><td>Minor</td><td>Dent on polarizer.</td><td>Unit: mm.
<table>
<tr><th>Diameter</th><th>Acceptable Q'ty</th></tr>
<tr><td>Φ≦0.2</td><td>Disregard</td></tr>
<tr><td>0.2 < Φ≦0.5</td><td>2(Distance>10mm)</td></tr>
<tr><td>0.50 < Φ</td><td>0</td></tr>
</table></td></tr>
<tr><td>11.5.3</td><td>Minor</td><td>Bubble in polarizer.</td><td>Unit: mm.
<table>
<tr><th>Diameter</th><th>Acceptable Q'ty</th></tr>
<tr><td>Φ≦0.2</td><td>Disregard</td></tr>
<tr><td>0.2 < Φ≦0.5</td><td>2(Distance>10mm)</td></tr>
<tr><td>0.50 < Φ</td><td>0</td></tr>
</table></td></tr>
</table>

<table>
<tr>
<td>11.5.4</td>
<td>Minor</td>
<td>Dot defect</td>
<td>
<table>
<tr><th>Items</th><th>Acceptable Q'ty</th></tr>
<tr><td>Bright dot</td><td>N ≦3</td></tr>
<tr><td>Dark dot</td><td>N ≦3</td></tr>
<tr><td>Total dot</td><td>N ≦6</td></tr>
</table>
Pixel define :


Dot Dot Dot
Note1: The definition of dot: The size of a defective dot over 1/2 of whole dot is regarded as one defective dot.
Note 2: Bright dot: Dots appear bright and unchanged in size in which LCD panel is displaying under black pattern.
Note 3: The bright dot defect must be visible through 2% ND filter
Note 4: Dark dot: Dots appear dark and unchanged in size in which LCD panel is displaying under pure red, green, blue
</td>
</tr>
<tr>
<td>11.5.5</td>
<td>Minor</td>
<td>LCD glass chipping.</td>
<td>


Y>S
Reject
</td>
</tr>
<tr>
<td>11.5.6</td>
<td>Minor</td>
<td>LCD glass chipping.</td>
<td>


X or Y>S
Reject
</td>
</tr>
<tr>
<td>11.5.7</td>
<td>Major</td>
<td>LCD glass crack.</td>
<td>


Y>(1/2) T
Reject
</td>
</tr>
<tr>
<td>11.5.8</td>
<td>Major</td>
<td>LCD glass scribe defect.</td>
<td>


1. a>L/3, A>1.5mm Reject
2. B : According to dimension
</td>
</tr>
</table>

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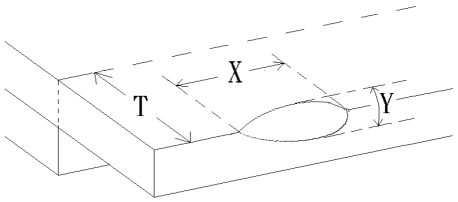
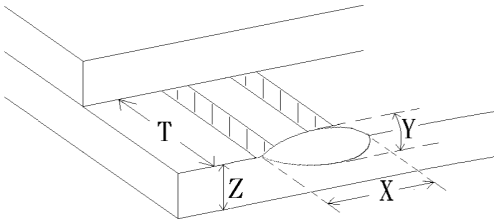
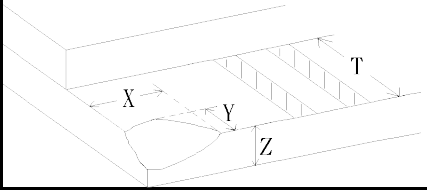
11.5.9	Minor	LCD glass chipping. (on the terminal area)		Φ = (x+y)/2>2.5mm Reject
11.5.10	Minor	LCD glass chipping. (on the terminal surface)		Y>(1/3)T Reject
11.5.11	Minor	LCD glass chipping.		Y>T Reject

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12. Handling Precautions

12.1 Mounting method

The TFT module consists of two thin glass plates with polarizes which easily be damaged. And since the module in so constructed as to be fixed by utilizing fitting holes in the printed circuit board.

Extreme care should be needed when handling the LCD modules.

12.2 Caution of LCD handling and cleaning

When cleaning the display surface, Use soft cloth with solvent

[Recommended below] and wipe lightly.

- Isopropyl alcohol.
- Ethyl alcohol.

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.
Do not use the following solvent:

- Water.
- Aromatics.

Do not wipe ITO pad area with the dry or hard materials that will damage the ITO patterns
Do not use the following solvent on the pad or prevent it from being contaminated:

- Soldering flux.
- Chlorine (Cl) , Sulfur (S).

If goods were sent without being silicon coated on the pad, ITO patterns could be damaged due to the corrosion as time goes on.

If ITO corrosion happen by miss-handling or using some materials such as Chlorine (CI), Sulfur (S) from customer, Responsibility is on customer.

12.3 Caution against static charge

The LCD module use C-MOS LSI drivers, so we recommended that you:

Connect any unused input terminal to POWER or GROUND, do not input any signals before power is turned on, and ground your body, work/assembly areas, and assembly equipment to protect against static electricity.

12.4 packing

- Module employs LCD elements and must be treated as such.
- Avoid intense shock and falls from a height.
- To prevent modules from degradation, do not operate or store them exposed direct to sunshine or high temperature/humidity.

12.5 Caution for operation

- It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life.
- An electro chemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- Response time will be extremely delayed at lower temperature then the operating temperature range and on the other hand at higher temperature LCD's how dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified operation temperature.
- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- Slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.

Usage under the maximum operating temperature, 50%Rh or less is required.

12.6 storing

In the case of storing for a long period of time for instance, for years for the purpose or replacement use, the following ways are recommended.

- Storage in a polyethylene bag with the opening sealed so as not to enter fresh air outside in it. And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light's keeping the storage temperature range.
- Storing with no touch on polarizer surface by the anything else.

[It is recommended to store them as they have been contained in the inner container at the time of delivery from us.

12.7 Safety

- It is recommendable to crash damaged or unnecessary LCD's into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.
- When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water.

13. Precaution for Use

13.1

A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity. Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.

13.2

On the following occasions, the handing of problem should be decided through discussion and agreement between responsible of the both parties.

- When a question is arisen in this specification
- When a new problem is arisen which is not specified in this specifications
- When an inspection specifications change or operating condition change in customer is reported to TFT , and some problem is arisen in this specification due to the change
- When a new problem is arisen at the customer's operating set for sample evaluation in the customer site.

- END